

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF: U.S. Patent No. 7,237,084

USPTO CONFIRMATION CODE: 3575

APPLICATION NO.: 10/694,302

FILED: Oct. 27, 2003

EXAMINER: Pierre-Michel Bataille

GROUP ART UNIT: 2186

FOR: METHOD AND PROGRAM PRODUCT FOR AVOIDING CACHE  
CONGESTION BY OFFSETTING ADDRESSES WHILE ALLOCATING MEMORY

ATTORNEY DOCKET NUMBER: 200309576-1

37 CFR 1.322 & 37 CFR 1.323 REQUEST FOR CERTIFICATE OF CORRECTION

HONORABLE COMMISSIONER OF PATENTS & TRADEMARKS

Sir:

The following is a request for a certificate of correction in Serial Number  
10/694,302, now Patent Number 7,237,084.

A certificate of correction under 35 USC 254 is respectfully requested in the  
above-identified patent.

☒ All errors were the fault of the USPTO, no fee required. In the event that a further  
fee is required, please charge the amount to Deposit Account No. 082025.

☐ All errors were the fault of the applicant and, accordingly, please charge \$100.00  
to our Deposit Account No. 082025. In the event that a further fee is required, please  
charge the amount to the same Deposit Account.

☐ The errors were the fault of both the applicant and USPTO and, accordingly,  
please charge \$100.00 to our Deposit Account No. 082025. In the event that a further fee  
is required, please charge the amount to the same Deposit Account.

The exact locations where the errors appear in the patent and patent application  
are as follows:

On the face page, in field (56), under "Other Publications", in column 2, lines 1-4, delete "Chavarria-Miranda, Daniel and John Mellor-Crummey, "An Evaluation of Data-Parallel Compiler Support for Line-Sweep Applications". Apr. 2003, Journal of Instruction-Level Parallelism 5, pp. 17-28.\*" (Repeated entry)

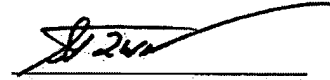
(List of References cited by the examiner dated Oct. 26, 2005, page 1 of 1, under "Non-Patent Documents", entry 1, lines 1-2)

The requested corrections are attached on Form PTO 1050.

Respectfully Submitted

DATE

6/24/2008



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Registration No.: 44,395

Attorney/Agent of Record

**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**

Page 1 of 1

PATENT NO : 7,237,084  
APPLICATION NO. : 10/694,302  
ISSUE DATE : Jun. 26, 2007  
INVENTOR(S) : Douglas Vincent Larson, et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (56), under "Other Publications", in column 2, lines 1- 4, delete "Chavarria-Miranda, Daniel and John Mellor-Crummey, "An Evaluation of Data-Parallel Compiler Support for Line-Sweep Applications". Apr. 2003, Journal of Instruction-Level Parallelism 5, pp. 17-28.\*"

**MAILING ADDRESS OF SENDER (Please do not use customer number below):**

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This collection of information is required by 37 CFR 1.322, and 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patent, P.O. Box 1450, Alexandria, VA 22313-1450.

*If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.*